

## EEE 324 Digital Electronics Circuit

Number Systems and Codes. Logic Gate Simplification of Logic expressions using Boolean Algebra. Simplification of logic expressions using Karnaugh method. Design combination circuit. Flip – Flops in the design of counters, registers and timers. Switching and waves shipping circuit. General of non sinusoidal signal (multi vibrators). Introduction to ADC and DAC. Designed of Logic Gates (Diode, DTL, TTL, ECL e.t.c)